

FTF-S85G-S55L-080D

SFP+8GBase-ZR, 1550nm, single-mode, 80km



Description

FTF-S85G-S55L-080D series SFP+ transceiver can be used to setup a reliable, high speed serial data link over single-mode fiber. Maximum link span can reach 80km. Casing made fully from metal alloys ensures very good EMI immunity. Module is fully compliant with SFP+ MSA specification and it is available in two hardware versions:

Model	Operating case temperature
FTF-S85G-S55L-080D	0~70°C
FTF-S85G-S55L-080DI	-40~85°C

Host device can access module internal EEPROM memory and DDMI via I²C interface.

Built-in digital diagnostic interface (DOM, DDMI) allows a network administrator to monitor module parameters such as: transmitted and received optical power, temperature, supply voltage and laser current. Those information and data are very helpful e.g. in prediction and prevention of connection failures. A module is available in various dedicated versions, which can be compatible with devices from vendors such as Cisco, HP, Juniper, Extreme Networks, Alcatel-Lucent, 3Com, Linksys and more.

Applications

- Fiber Channel (8G FC)
- SONET/SDH (OC-192/STM64)





Key features

- LC Duplex receptacle
- Transmission distance up to 80km*
- EML laser diode 1550nm transmitter, APD receiver
- Throughput up to 8.5Gb/s
- Fully compliant with SFP+ MSA INF-8431 and INF-8472 rev. 11.3
- Hot-Pluggable
- RoHS compliant
- Class 1 laser safety
- Low power dissipation (<1W)
- Metal case for low EMI
- Operating case temperature* : 0~70°C / -40~85°C

Specification

Supported transmission technology	Output power
Fibre Channel	<u>0~+5dBm</u>
Speed supported for Ethernet technology	Receiver sensitivity
N/A	-24dBm
Speed supported for Fibre Channel technology	Power supply voltage
8.5Gbps	3.3V
Transmission medium	Total power consumption
Single-mode fiber 9/125µm	< 1W
Transmission distance**	Operating environment – temperature*
<u>80km</u>	0~70°C / -40~+85°C
Receptacle type	Operating environment - humidity
LC Duplex	5~95% non-condensing
Wavelength	Dimensions
1550nm	Compliant with SFP+ Multi-Source Agreement

* - standard / industrial version

** - transmission distance depends on optical link attenuation

Fibrain Co., Ltd. Address: ul. Wspólna 4A, 35-205 Rzeszów, Poland Tel: +48 17 86-60-811, +48 17 86-60-812 Fax: +48 17 86-60-810 Email: info@fibrain.com http://www.fibrain.com





Detailed technical specification

Pin Description

Pin	Name	Function/Description	Engagement order	Notes
1	VeeT	Transmitter Ground	1	8
2	TX Fault	Transmitter Fault Indication	3	1
3	TX Disable	Transmitter Disable-Module disables on high or open	3	2
4	SDA	Module Definition 2-Two wire serial ID interface	3	3
5	SCL	Module Definition 1-Two wire serial ID interface	3	3
6	MOD_ABS	Module Definition 0-Grounded in module	3	3
7	RS0	Not Used	3	-
8	RX_LOS	Receiver Loss of Signal	3	4
9	RS1	Not Used	1	-
10	VeeR	Receiver Ground	1	8
11	VeeR	Receiver Ground	1	8
12	RD-	Inverse Received Data out	3	5
13	RD+	Received Data out	3	5
14	VeeR	Receiver Ground	1	8
15	VccR	Receiver Power - +3.3V±5%	2	6
16	VccT	Transmitter Power - +3.3 V±5%	2	6
17	VeeT	Transmitter Ground	1	8
18	TD+	Transmitter Data In	3	7
19	TD-	Inverse Transmitter Data In	3	7
20	VeeT	Transmitter Ground	1	8

Notes:

1. TX Fault is open collector/drain output which should be pulled up externally with a $4.7K\Omega$ -10K Ω resistor on the host board. When high, this output indicates a laser fault of some kind. Low indicates normal operation.

2. TX Disable input is used to shut down the laser output per the state table below. It is pulled up to Vcc within the module.

Low (0 – 0.8V): Transmitter on

Open or High (2.0 – VccT): Transmitter Disabled

3. Mod-Def 0, 1, 2. These are the module definition pins. They should be pulled up to Vcc with a $4.7K\Omega$ -10K Ω resistor on the host board Mod-Def 0 is grounded by the module to indicate that the module is present.

Mod-Def 1 is clock line of two wire serial interface for optional serial ID.

Mod-Def 2 is data line of two wire serial interface for optional serial ID.

4. LOS - when high, output indicates the received optical power is below the worst case receiver sensitivity. Low indicates normal operation.

- 5. These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω differential at the user SERDES. The AC coupling is done inside the module and thus not required on the host board.
- 6. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V±5% at the SFP connector pin. The in-rush current will typically be no more than 30mA above steady state supply current after 500ns.
- 7. TD-/+: These are the differential transmitter inputs. They are AC coupled differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on host board.
- 8. The module signal grounds are isolated from the module case.





Electrical parameters

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Transmitter Differential Input Volt	+/-TX_DAT	180		700	mV p-p	1
Receiver Differential Output Volt	+/-RX_DAT	180		850	mV p-p	2
Tx_Disable Input Voltage – Low	VIL	0		0.8	V	
Tx_Disable Input Voltage – High	Vih	2.0		Vcc	V	
Tx_Fault Output Voltage – Low	Vol	0		0.8	V	3
Tx_Fault Output Voltage – High	Vон	2.0		Vcc	V	3
Rx_LOS Output Voltage- Low	V _{OL}	0		0.8	V	3
Rx_LOS Output Voltage- High	V _{OH}	2.0		Vcc	V	3
Throughput	В			11.3	Gb/s	
Total current requirement				500	mA	

Transmitter parameters

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes	
Central wavelength	λς	1530	1550	1565	nm		
Spectral width	Δλ			0.3	nm		
Launch optical power	P₀	0		+5	dBm	4	
Extinction ratio	EX	6			dB		
Dispersion penalty				3	dB		
Optical Return Loss Tolerance	ORLT	20			dB		
Optical rise/fall time	Trise/Tfall			30	ps	5	
Eye diagram	Com	Compliant with IEEE802.3-2005 8G BASE-ZR					

Receiver parameters

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Sensitivity	P _{min}			-24	dBm	6
Stressed Sensitivity (OMA)				-21	dBm	
Central wavelength	λς	1500		1600	nm	
Receiver overload	P _{MAX}	-7			dBm	6
RX_LOS Asserted	SA	-35			dBm	
RX_LOS De-Asserted	SD			-25	dBm	
RX_LOS Hysteresis	-		3.0		dB	

Notes:

- 1. Internally AC coupled and terminated to 100Ω differential load.
- 2. Internally AC coupled, but requires a 100Ω differential termination or internal to Serializer/Deserializer.
- It is open collector/drain output which should be pulled up externally to Vcc with a 4.7KΩ-10KΩ resistor on the host board. LOS: logic 0 indicates normal operation; logic 1 indicates no signal detected.
- 4. Optical power is launched into SMF
- 5. 20-80%
- 6. Measured with PRBS 2³¹-1 at test pattern @10.3125Gbps.

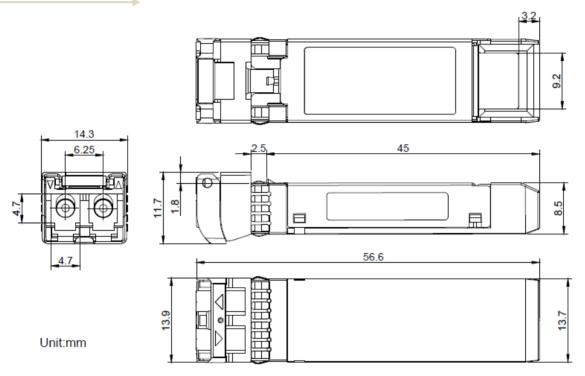
Fibrain Co., Ltd. Address: ul. Wspólna 4A, 35-205 Rzeszów, Poland Tel: +48 17 86-60-811, +48 17 86-60-812 Fax: +48 17 86-60-810 Email: info@fibrain.com http://www.fibrain.com

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Mechanical specification



Recommended environment conditions

Parameter	Symbol	Min	Тур	Max	Unit
Operating Temperature Range (industrial)	Т	-40	-	85	0C
Operating Temperature Range (standard)	Т	0	25	70	0C
Supply Voltage	Vcc	3.135	3.3	3.465	V
Relative Humidity	RH	5	-	95	%

Ordering information

FTF-S85G-S55L-080**D**– 1550nm, 80km, single-mode, LC duplex, **DDMI**, commercial temperature (0~70°C) FTF-S85G-S55L-080**DI**– 1550nm, 80km, single-mode, LC duplex, **DDMI**, **extended temperature** (-40~85°C)

For further information regarding host device PCB layout recommendation, power supply requirements, EEPROM memory map, DDMI specification please check: <u>SFF-8472</u> - Description of EEPROM and Digital Diagnostic Monitoring Interface and SFF-8431 - Technical specification for SFP+ transceiver

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Fibrain Co., Ltd. Address: ul. Wspólna 4A, 35-205 Rzeszów, Poland Tel: +48 17 86-60-811, +48 17 86-60-812 Fax: +48 17 86-60-810 Email: info@fibrain.com http://www.fibrain.com

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